

SYSTEM AND METHOD FOR DESIGNING A CIRCUIT WHEREIN A SINGLE
TIMING ANALYSIS ENSURES ADEQUATE PERFORMANCE
IN MULTIPLE APPLICATIONS

5 ABSTRACT OF THE DISCLOSURE

 A disclosed method for designing a circuit having multiple conductors includes selecting first and second circuit operating points corresponding to first and second circuit applications, respectively. A performance difference between circuit operation at the first and second circuit operating points is determined and used to compute a factor. The
10 factor is applied to resistance values of the conductors, thereby producing modified conductor resistance values. A timing analysis of the circuit is performed using the modified conductor resistance values. A computer program product is described including computer program code for carrying out some or all of the operations of the method. An apparatus for designing the circuit includes means for applying the factor to
15 the resistance values of the conductors and for performing the timing analysis of the circuit. A described timing analysis system includes a memory system and a central processing unit coupled to the memory system.